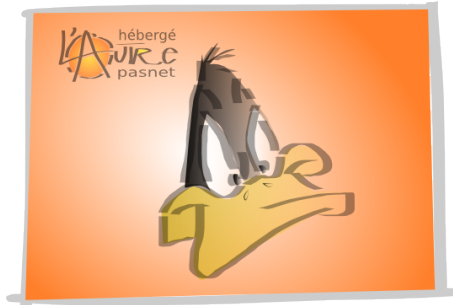


<http://www.coincoin.fr.eu.org/?Sans-titre-33499>



Fil Threadreaderapp

- 6- Webographie -

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Here's an unusual chip, a 7400-series decoder implemented with a gate array. It has over 1500 transistors in orderly rows, but most of them are entirely unused. Why build a chip in such an inefficient way ? Let's take...